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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/623,587	07/22/2003	Bong-Seok Kim	249/236 DIV	5855

TITLE OF INVENTION: MERGED MEMORY AND LOGIC SEMICONDUCTOR DEVICE OF SALICIDED DUAL GATE STRUCTURE INCLUDING EMBEDD MEMORY OF SELF-ALIGNED CONTACT STRUCTURE AND MANUFACTURING METHOD THEREOF

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1330	\$300	\$1630 10/01/2004 TBESHAH2 0	10/26/2004 0000055 10623587
EXA	MINER	ART UNIT	CLASS-SUBCLASS		
PHAM	I, LONG	2814	438-241000	-J01 FC:1501 02 FC:1504	1330.00 OF 300.00 OF
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(A) NAME OF ASSIGNEE

Samsung Electronics Co., Ltd.

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Kyungki-do, Republic of Korea

Please check the appropriate assignee category or categories (will not be p	rinted on the patent); individual corporation or other private group entity governm		
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9/29/04

(Date)

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